



PATENT  
5500-91600/TT5403

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/700,033  
Filed: November 3, 2003  
Inventors:  
Gregory William Smaus  
Mitchell Alsup

Title: Instruction Cache Prefetch  
Based on Trace Cache  
Eviction

§ Examiner: Cody, Dillon J.  
§ Group/Art Unit: 2183  
§ Atty. Dkt. No: 5500-91600  
§  
§  
§  
§  
§  
§  
§  
§  
§  
§

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Robert C. Kowert

Printed Name



Signature

June 14, 2006

Date

RESPONSE TO OFFICE ACTION OF MARCH 14, 2006

**Mail Stop Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of March 14, 2006, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.